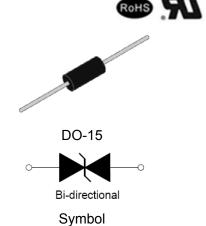
# JIEJIE MICROELECTRONICS CO., Ltd

# P6KE520CA 600W Transient Voltage Suppressor

Rev.1.2

#### **DESCRIPTION**

The P6KE520CA of high current transient suppressors are designed for A.C. line protection and high power DC bus clamping applications. They provide a clamping voltage lower than the avalanche voltage. Therefore, any voltage rise due to increased current conduction is contained to a minimum, providing the best possible protection level. They can also be connected in series and/or parallel to create very high capacity protection solutions.



#### **FEATURES**

- ♦ Low incremental surge resistance.
- ♦ Excellent clamping capability.
- ♦ High temperature wave soldering: 265°C/10s at terminals.
- ♦ Plastic package has underwriters laboratory flammability 94V-0.
- ♦ 600W peak pulse power capability at 10/1000µs waveform.
- ♦ Meets MSL level 1, per J-STD-020, LF maximum peak of 260°C.
- ♦ Terminal: solder plated, solderable per J-STD-002.
- ♦ Fast response time: typically less than 1.0ps from 0V to V<sub>BR</sub> min.
- ♦ UL 497B item recognized. (File No.:E480698).
- ♦ IEC61000-4-2 (ESD) ±30kV (air), ±30kV (contact).

#### **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, RH=45%-75%, unless otherwise noted)

Parameter	Symbol	Value	Unit
Operating junction and storage temperature range	T <sub>J</sub> ,T <sub>STG</sub>	-55 to +175	$^{\circ}$
Peak pulse power dissipation at 10/1000µs waveform	P <sub>PP</sub>	600	W
Steady state power dissipation at T <sub>L</sub> =75°C	P <sub>M(AV)</sub>	5.0	W
Typical thermal resistance junction to lead	ReJL	20	°C/W
Typical thermal resistance junction to ambient	R <sub>θ</sub> JA	75	°C/W

### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C)

Part Number	V <sub>R</sub>	I <sub>R</sub> @V <sub>R</sub>	V <sub>BR</sub> @I <sub>T</sub>		Ι <sub>Τ</sub>	V <sub>C</sub> @I <sub>PP</sub>	l <sub>PP</sub> <sup>①</sup>
Bi-Polar	V	max(µA)	min(V)	max(V)	mA	max(V)	Α
P6KE520CA	445	1	494	546	1	710	0.85

① Surge waveform: 10/1000µs

V<sub>R</sub>: Stand-off voltage -- Maximum voltage that can be applied

V<sub>BR</sub>: Breakdown voltage

V<sub>C</sub>: Clamping voltage -- Peak voltage measured across the suppressor at a specified I<sub>PP</sub>

IR: Reverse leakage current

## RATINGS AND V-I CHARACTERISTICS CURVES (T<sub>A</sub>=25°C, unless otherwise noted)

FIG.1:V- I curve characteristics (Bi-directional)

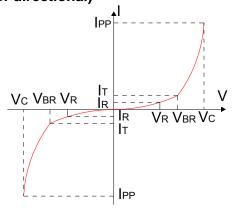


FIG.2: Pulse waveform

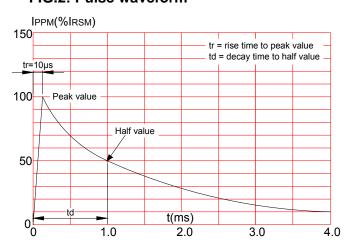
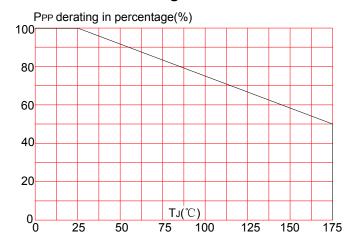


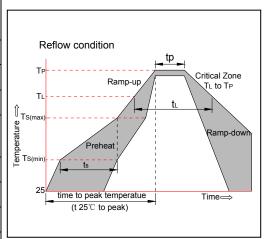
FIG.3: Pulse derating curve





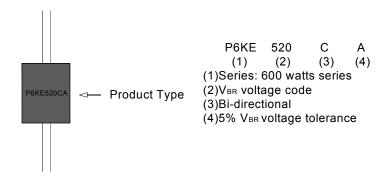
#### **SOLDERING PARAMETERS**

Reflow Condition		Pb-Free assembly (see figure at right)	
	-Temperature Min (T <sub>s(min)</sub> )	+150°C	
Pre Heat	-Temperature Max(T <sub>s(max)</sub> )	+200℃	
Tical	-Time (Min to Max) (t <sub>s</sub> )	60-180 secs.	
Average (T <sub>L</sub> )to pe	ramp up rate (Liquidus Temp eak)	3℃/sec. Max	
T <sub>s(max)</sub> to	T <sub>L</sub> - Ramp-up Rate	3°C/sec. Max	
Deflow	-Temperature(T <sub>L</sub> )(Liquidus)	<b>+217</b> ℃	
Reflow	-Temperature(t∟)	60-150 secs.	
Peak Tem	ηρ (T <sub>p</sub> )	+260(+0/-5)°C	
Time with	in 5℃of actual Peak Temp (t <sub>p</sub> )	20-40secs.	
Ramp-do	wn Rate	6°C/sec. Max	
Time 25°	to Peak Temp (T <sub>P</sub> )	8 min. Max	
Do not ex	ceed	+260℃	

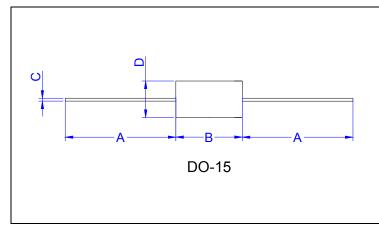


Flow/Wave Soldering(Solder Dipping)				
Peak temperature 265°C				
Dipping time	10 sec.			
Soldering	1 time			

# MARKING & ORDERING INFORMATION



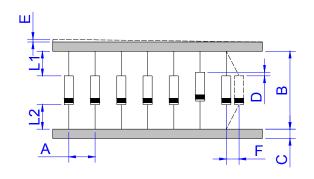
### PACKAGE MECHANICAL DATA

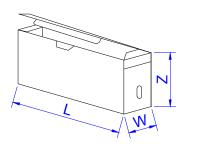


	Dimensions				
Ref.	Millimeters		Inc	hes	
	Min.	Max.	Min.	Max.	
А	25.40	ı	1.000	ı	
В	5.80	7.62	0.228	0.300	
С	0.71	0.86	0.028	0.034	
D	2.60	3.60	0.102	0.142	



### **TAPE AND BOX SPECIFICATION-DO-15**





Def	Dimensions			
Ref.	Millimeters	Inches		
Α	5.0±0.5	0.197±0.020		
В	53.0±1.5	2.087±0.059		
С	6.0±0.5	0.236±0.020		
D	1.2(MAX)	0.047(MAX)		
E	0.8(MAX)	0.031(MAX)		
F	1.5(MAX)	0.059(MAX)		
L1-L2	1.0(MAX)	0.039(MAX)		
W	80±5.0	3.150±0.197		
L	250±5.0	9.843±0.197		
Z	115±5.0	4.528±0.197		

PART No.	UNIT WEIGHT (g/PCS) typ.	PER BOX (PCS)	PER CARTON (PCS)	DESCRIPTION
P6KE520CA	0.42	2,000	20,000	Вох

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.

Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information.

This document is the 1.2nd version which is made in 2-Aug.-2021. This document supersedes and replaces all information previously supplied.

is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd.

Copyright©2021 Jiangsu JieJie Microelectronics Co.,Ltd. Printed All rights reserved.